

Notice of References Cited	Application/Control No. 10/788,615		Applicant(s)/Patent Under Reexamination MUKKER ET AL.	
	Examiner Hong C. Kim		Art Unit 2185	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,809,550	09-1998	Shukla et al.	711/167
*	B	US-6,647,464	11-2003	Riedlinger et al.	711/122
*	C	US-6,732,236	05-2004	Favor, John G.	711/118
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Intel 865PE/865P Chipset Datasheet, Intel 865PE/865P Memory Controller Hub (MCH), May 2003, Figs. 2, 3, and 6 and page 19.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Idr